

WHAT IS CLAIMED IS:

1 1. A method for making an opening for electrical contact, the method
2 comprising:
3 performing a first etch through a first dielectric layer to form a first via and a
4 second dielectric layer;
5 filling the first via with a BARC material to form a first BARC layer;
6 performing a second etch on the first BARC layer to form a second BARC layer,
7 the second etch having a first etch rate in a first peripheral region of the second BARC layer and
8 a second etch rate in a first central region of the second BARC layer, the first peripheral region
9 located around a sidewall of the first via, the first central region located around a center of the
10 first via, the first etch rate being larger than the second etch rate, the first peripheral region
11 located higher than the first central region, a first top surface of the second BARC layer having
12 substantially a first convex shape;
13 performing a third etch through a second dielectric layer to form a trench and a
14 third BARC layer, the trench having a trench bottom surface, the trench bottom surface being
15 substantially free from any spike around a side surface of the third BARC layer, a second top
16 surface of the third BARC layer having substantially a second convex shape;
17 removing the third BARC layer to form a second via.

1 2. The method of claim 1 wherein the second etch uses a plasma, the plasma
2 having an electron temperature, the electron temperature being higher than 4 eV.

1 3. The method of claim 2 wherein the electron temperature is lower than 10
2 eV.

1 4. The method of claim 3 wherein the plasma has a gas pressure, the gas
2 pressure being lower than 40 mTorr.

1 5. The method of claim 4 wherein the second etch includes a dry etch, the
2 dry etch using a plurality of ions, the plurality of ions having a plurality of velocities
3 respectively, the plurality of velocities having a plurality of angles with respect to a direction

4 vertical to a top surface of the dielectric layer respectively, an average magnitude of the plurality
5 of angles being smaller than 10 degrees.

1 6. The method of claim 5 wherein the average magnitude of the plurality of
2 angles being smaller than 5 degrees.

1 7. The method of claim 6 wherein the second etch uses a plasma, the plasma
2 having a plasma density, the plasma density exceeding 5×10^{16} ions/m³.

1 8. The method of claim 1 wherein the second etch uses an oxygen gas.

1 9. The method of claim 1 further comprising filling the trench and the second
2 via with a conductive material.

1 10. The method of claim 1 wherein the first dielectric layer comprises at least
2 one selected from silicon oxide, FSG, and silicon nitride.

1 11. A method for making an electrical contact, the method comprising:
2 performing a first etch through a first protective layer and a first dielectric layer to
3 form a first via, a second protective layer and a second dielectric layer, the first protective layer
4 located on the first dielectric layer;
5 filling the first via with a BARC material to form a first BARC layer;
6 performing a second etch on the first BARC layer to form a second BARC layer,
7 the second etch having a first etch rate in a first peripheral region of the second BARC layer and
8 a second etch rate in a first central region of the second BARC layer, the first peripheral region
9 located around a sidewall of the first via, the first central region located around a center of the
10 first via, the first etch rate being larger than the second etch rate, the first peripheral region
11 located higher than the first central region;
12 performing a third etch through a second protective layer and a second dielectric
13 layer to form a trench and a third BARC layer, the trench having a trench bottom surface, the
14 trench bottom surface being substantially free from any spike around a side surface of the third
15 BARC layer;
16 removing the third BARC layer to form a second via, a cross-section of the
17 second via being smaller than a cross-section of the trench;

18 performing a fourth etch through a stop layer to form a third via, the dielectric
19 layer located on the stop layer;
20 filling the trench and the third via with a conductive material.

1 12. The method of claim 11 wherein the second etch uses a plasma, the
2 plasma having an electron temperature, the electron temperature being higher than 4 eV.

1 13. The method of claim 12 wherein the electron temperature is lower than 10
2 eV.

1 14. The method of claim 12 wherein the plasma has a gas pressure, the gas
2 pressure being lower than 40 mTorr.

1 15. The method of claim 11 wherein the second etch includes a dry etch, the
2 dry etch using a plurality of ions, the plurality of ions having a plurality of velocities
3 respectively, the plurality of velocities having a plurality of angles with respect to a direction
4 vertical to a top surface of the dielectric layer respectively, an average magnitude of the plurality
5 of angles being smaller than 10 degrees.

1 16. The method of claim 15 wherein the average magnitude of the plurality of
2 angles being smaller than 5 degrees.

1 17. The method of claim 11 wherein the second etch uses a plasma, the
2 plasma having a plasma density, the plasma density exceeding 5×10^{16} ions/m³.

1 18. The method of claim 11 wherein the first dielectric layer comprises at least
2 one selected from silicon oxide, FSG, and silicon nitride.

1 19. The method of claim 11 wherein the conductive material comprises at
2 least one selected from a group consisting of copper, aluminum, tungsten, and polysilicon.

1 20. The method of claim 11 wherein the protective layer comprises silicon
2 oxynitride and the stop layer comprises silicon nitride.